

Fig.1

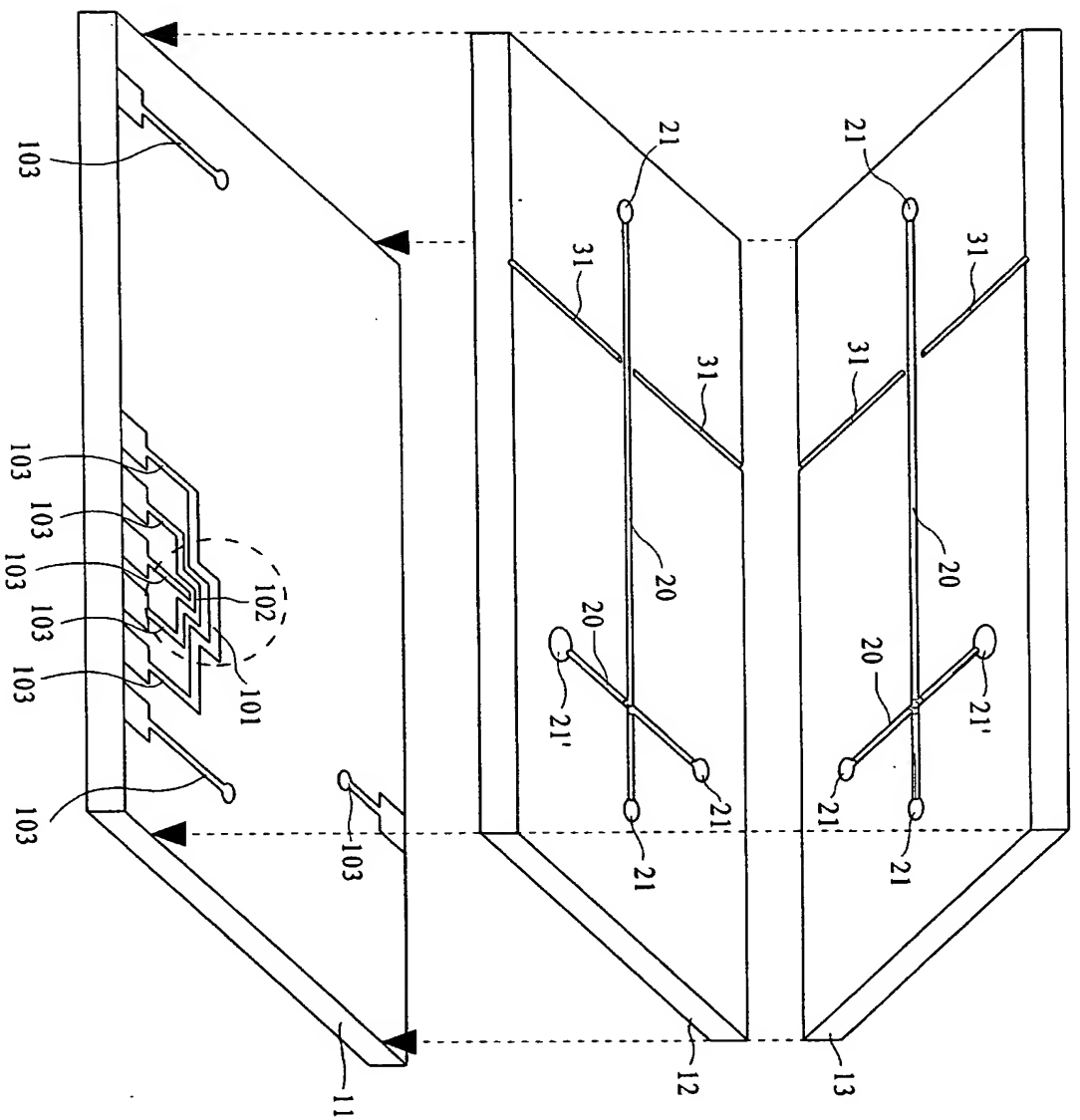


Fig. 2

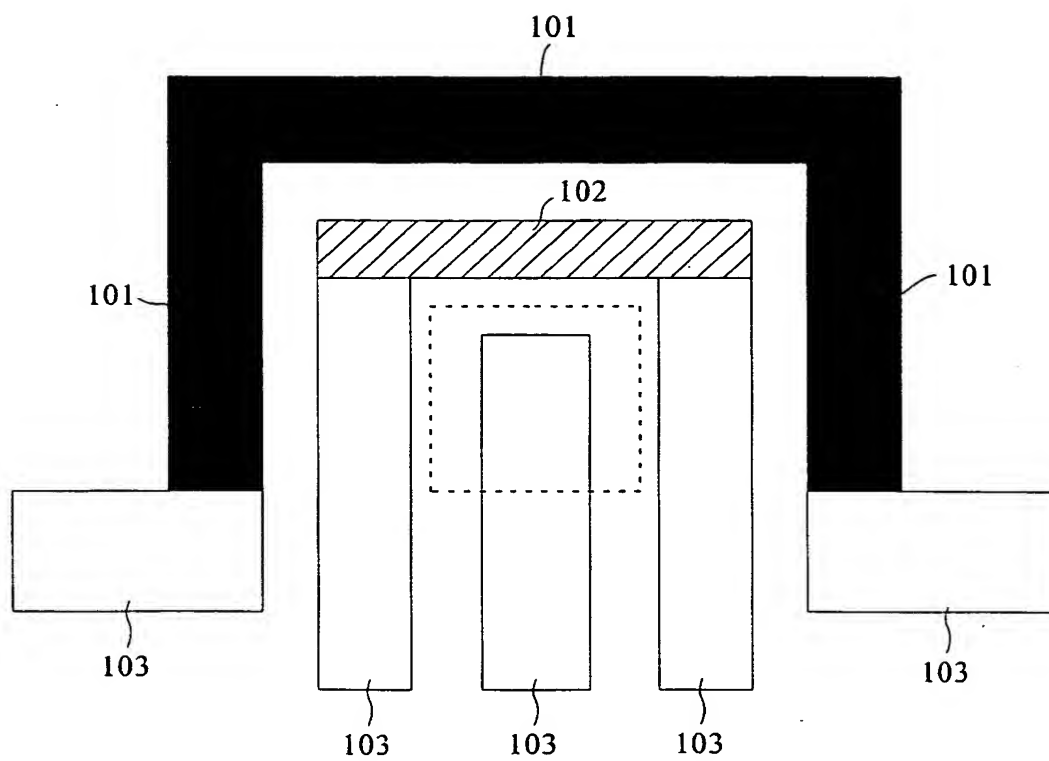
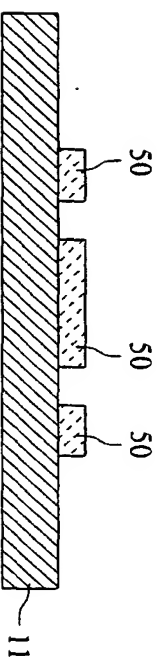
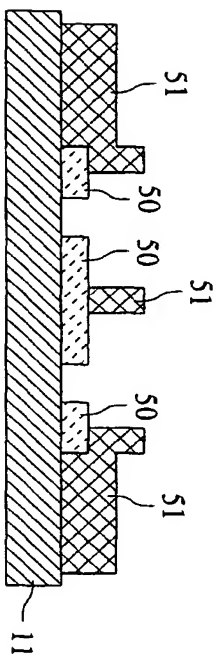


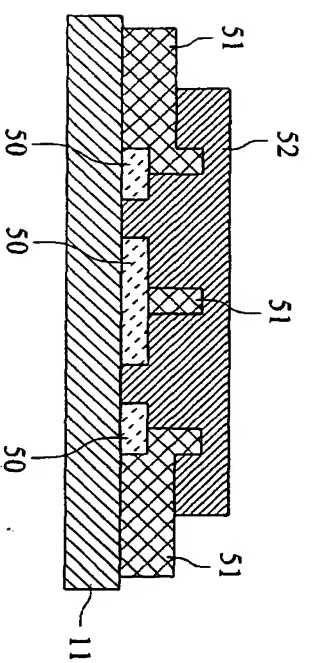
Fig.3



(A) Deposit an electric resistance



(B) Deposit a conductivity layer



(C) Coating an insulating layer

Fig.4

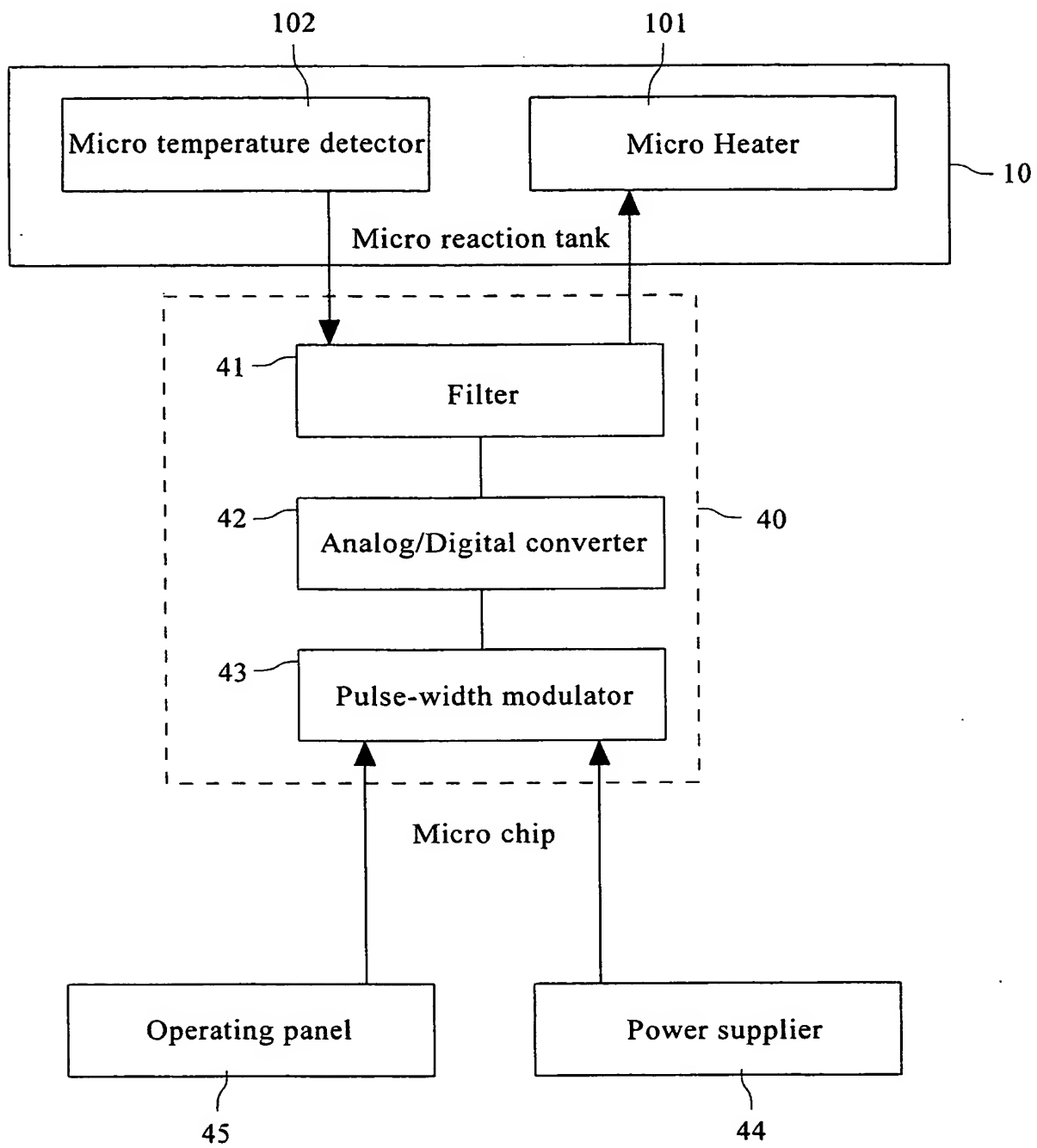


Fig.5

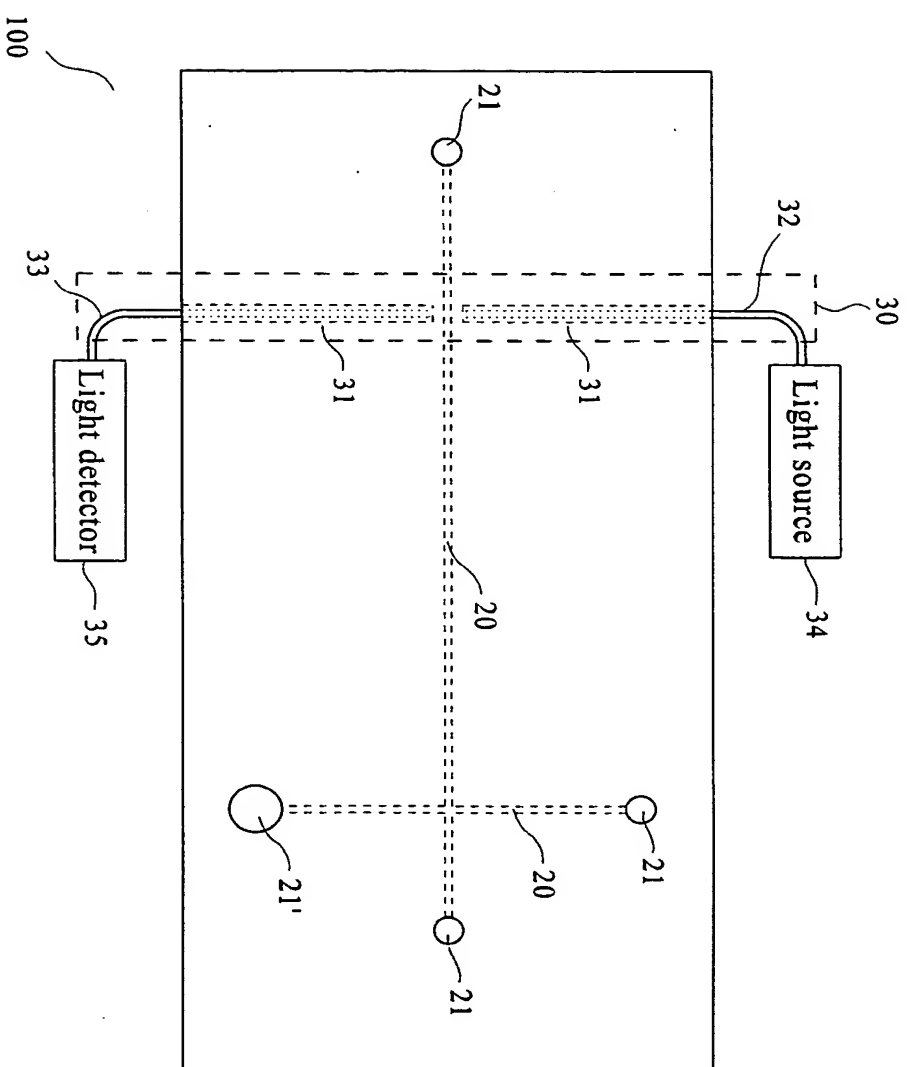


Fig.6

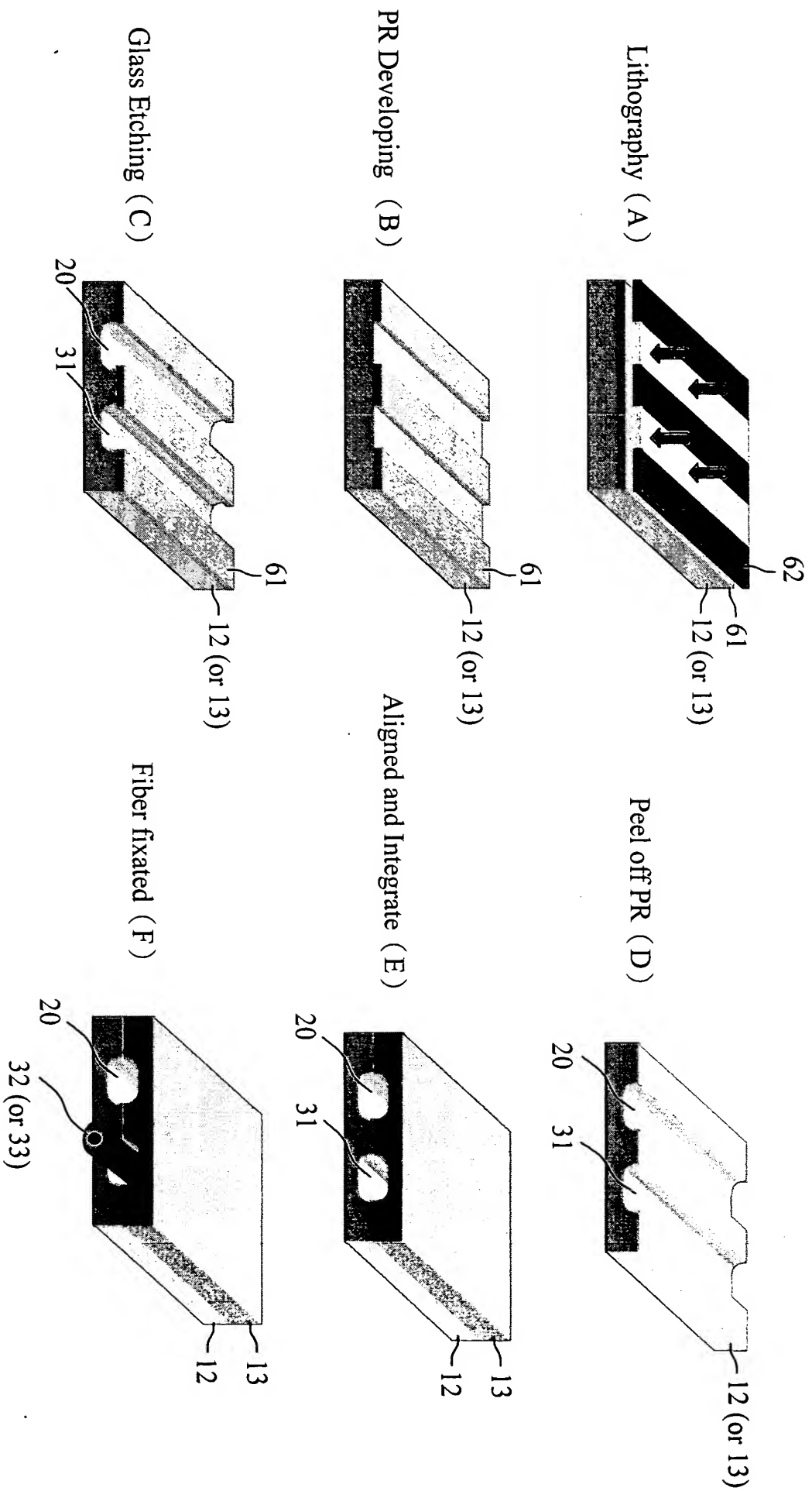


Fig. 7

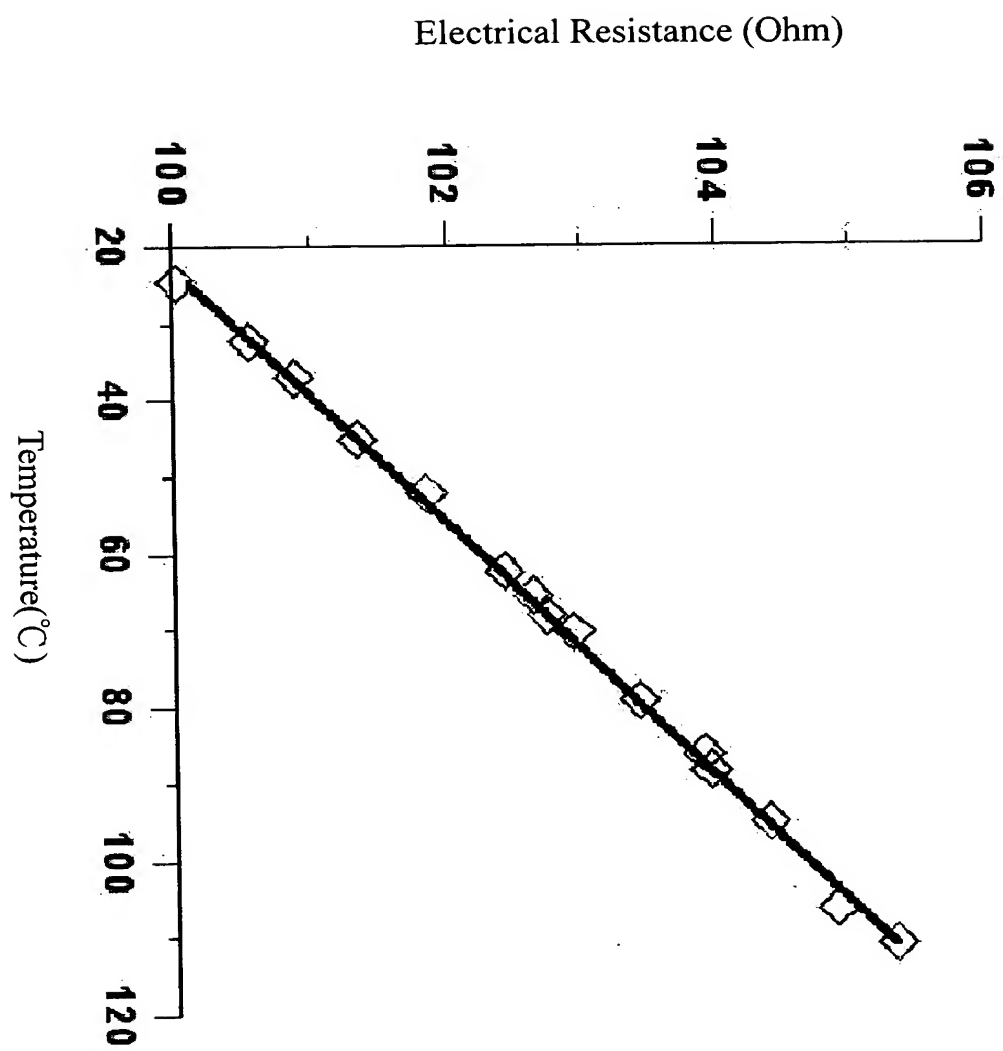


Fig. 8



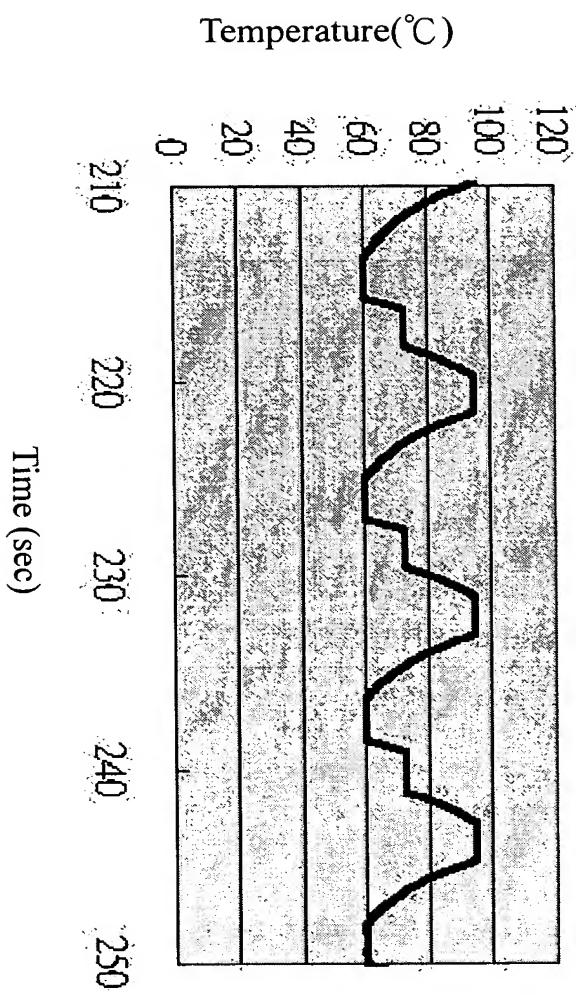


Fig. 9

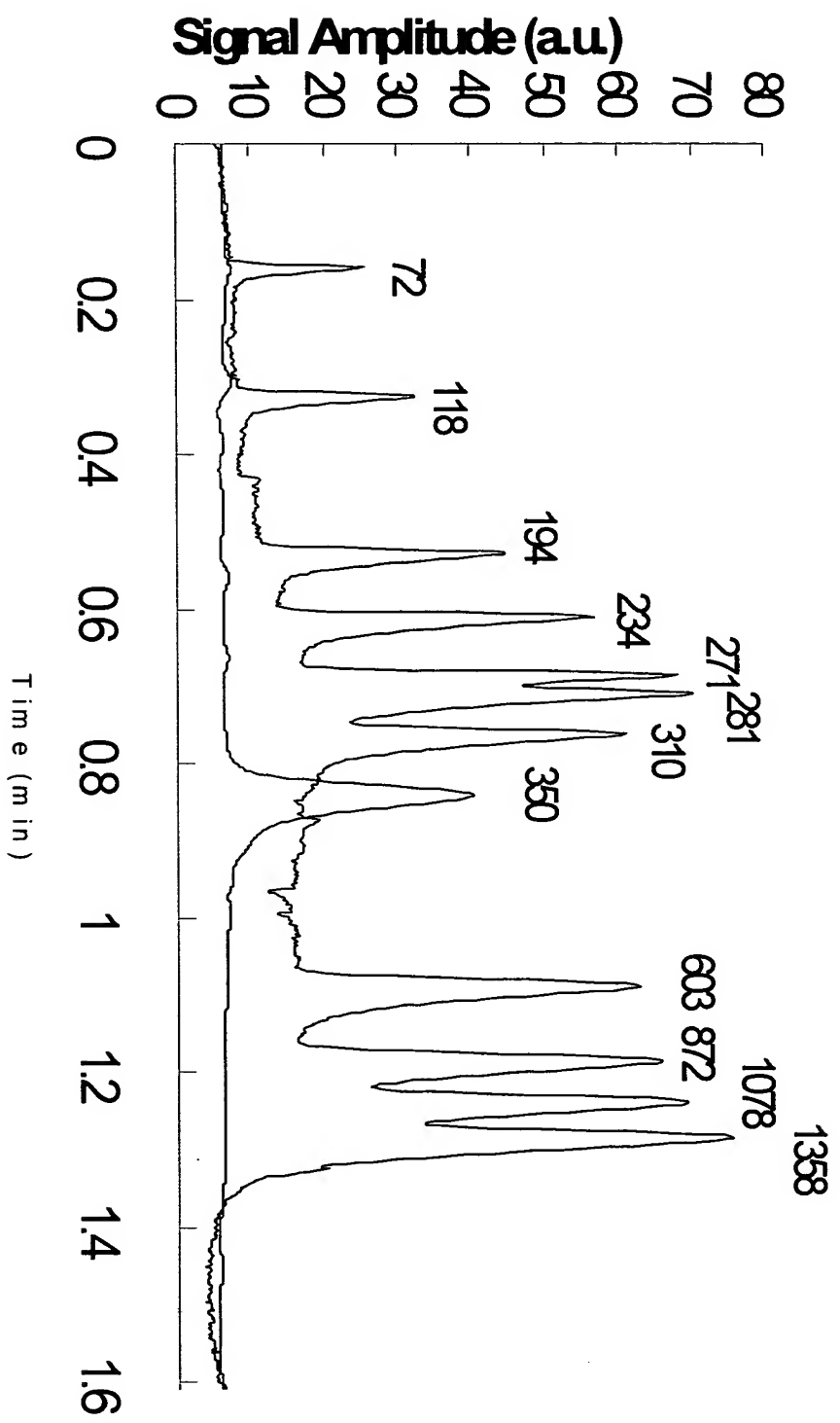


Fig. 10